




M.A.M. COLLEGE OF ENGINEERING AND TECHNOLOGY
Siruganur, Tiruchirappalli 621 105.
(Approved by AICTE & Affiliated to Anna University)
FACULTY PROFILE

Name	V.Kavitha	Email	kavi.kavitha30@gmail.com	
Qualification	M.E	AU Faculty Code	8120244	
Designation	Assistant Professor	Mobile Number	9677348185,8825705981	
Area of Specialization	VLSI	Experience at MAMCET	6 Months	
Experience	3.5 Years	Joining Date	02.07.2018	

Education Qualification

Degree	Name of the Institution	University	Year of Passing
Ph.D.	Anna University	Anna University, Chennai	Pursuing
M.E.	College of Engineering, Guindy	Anna University, Chennai	2015
B.E.	Hindusthan College of Engineering and Technology, Coimbatore	Anna University, Chennai	2007

Experience Details

Institution/ Company	Designation / Position	Duration	Experience in Years	Cumulative Experience
J.J College of Engineering and Technology, Trichy	Assistant Professor	1.5 Years	1.5 Years	1.5 Years
Mount Zion College of Engineering and Technology, Pudukkottai	Assistant Professor	1.5 Years	1.5 Years	3 Years
M.A.M College of Engineering and Technology, Trichy	Assistant Professor	6 Months	0.5 Year	3.5 Years

Publications Details

Category	Journal	Conference	Total
National			1
International	2		3
Consultancy		Total Publications	6
Product Development		Ph.D. Guidance	-

Workshop/Conference/FDP/Seminar Participation : Workshops - 13 (Attended), 2(Conducted)

FDP - 6 (Attended), Seminar – 3.

- Attended 2 week Faculty Development Programme on “Analog IC Design - VLSI” Organized by “IIT Bombay” and
- Attended at “Mount Zion College of Engineering and Technology” held on November 2016.
- Attended 2 week Faculty Development Programme on “Digital Signal Processing” attended at “Government College of Engineering, Sri Rangam” held on December 2016.
- Attended and certified in IIT Bombay FDP (FDP101x) with a consolidated Score of 74% on “Foundation program in ICT for Education” ended on 27 th October 2018.
- Attended and certified in IIT Bombay FDP (FDP201x) on “Pedagogy for online and Blended Teaching- Learning process”.
- Attended 1 day Faculty Development Programme on “Digital IC Design” conducted by “Easwari Engineering College, Chennai” on 23 rd March 2014.
- Attended 1 day Faculty Development Programme on “Embedded systems” conducted by “J.J College of Engineering and Technology”.
- Attended the 2 days workshop on “Analog and Digital IC Design using Cadence” in Easwari Engineering College held on 21 st and 22 nd March 2015.
- Attended the 2 days workshop on “Analog IC Design using Cadence” in VIT, Chennai held on 21 st and 22 nd November 2014.
- Attended the 2 days workshop on “Analog and Digital IC Design using Synopsis” in VIT, Vellore held on 20 th and 21 st August 2014.
- Attended 3 days Workshop on “CAD Tools in VLSI” in SRM University, held on 20 th and 21 st July 2014.
- Attended 1 day Workshop on “Embedded Systems” in J.J College of Engineering and Technology, held on 20 th and 21 st November 2015.
- Attended One Day Workshop on VLSI Technology conducted by Hindusthan College of Engineering and Technology.
- Attended a workshop in “Computer Hardware Assembly and Networking” conducted by Hindusthan Institutions and Software Institute of Tamilnadu Private Limited.
- Completed a course in “Embedded System Design using ATMEL 89C51 and obtained ‘O’ grade”.
- Cleared mathworks-Deep learning Online Course with a Consolidated Score of 100%.
- Attended Code Tantra Online Course for “C and JAVA programming Language” Organized by IIT Bombay.
- Completed courses in “Computer Foundation” and “Advanced Computer Foundation” and got first class in the school level.
- Attended one day workshop on “The Art of Writing Research Articles” jointly Organized by M.A.M B-School and MAMCET on 19 th November 2018.
- Attended 2 days hands-on training workshop entitled “Xilinx vivado design Flow & High Level Synthesis Flow using Zynq” Organized by K.L.N. College of Engineering in association with CG-CoreEL Technologies, Bangalore from 25 th and 26 th September 2018.
- Completed a course in “Embedded System Design using ATMEL 89C51” and obtained ‘O’ grade.
- Completed courses in “Computer Foundation” and “Advanced Computer Foundation” and got first class in the school level.

Professional Body Membership : VSI (VLSI Society of India)

Awards and Achievement :

- Cleared Gate Exam in 2013
- Cleared Gate Exam in 2014
- Cleared **TANCET score of 53.23**
- Attended and passes the SENIOR of "ALL INDIA GENERAL KNOWLEDGE TEST 2001" conducted by Andhra Pradesh Public Library Association.
- Won in Debates, Extempore, Essay Writing in school.

International Conferences

1. Presented a paper in "**International conference**" held at **Karpagam College of Engineering-Coimbatore** on Spectrum Sensing System for Cognitive Radio.
2. Presented a paper in "**International conference**" held at **College of Engineering, Guindy** on A VLSI Architecture of Wireless Endoscopy System.
3. Presented a paper in "**International conference**" held at **P.S.G College of Technology –Coimbatore** on Image Detection using VLSI.

National Conference:

1. Presented a Paper in "**National Conference**" held in **Tamilnadu Engineering College – Coimbatore** on Realisation of Wireless Endoscopy System.

International Journals:

1. Published a Paper in International Journal "**International Journal of Advances in Engineering Research-IJAER**" on Spectrum Sensing System for Cognitive Radio.
2. Published a Paper "Pipelined ADC with A Transconductance Operational Amplifier In FINFET Technology', International Journal of Advanced Research in Electronics, Communication & Instrumentation Engineering and Development.

National Journals:

1. << >>

Others: