




**M.A.M. COLLEGE OF ENGINEERING AND TECHNOLOGY**  
**Siruganur, Tiruchirappalli**  
**621 105.**  
**(Approved by AICTE & Affiliated to Anna University)**  
**FACULTY PROFILE**

Name	G RAJESH	Email	rajeshme2k11@gmail.com	
Qualification	M.E	AU Faculty Code	8120242	
Designation	ASSISTANT PROFESSOR	Mobile Number	8526010161	
Area of Specialization	EMBEDDED SYSTEMS, VLSI DESIGN	Experience at MAMCET	1 YEAR 1 MONTH	
Experience	6 YEARS 8 MONTHS	Joining Date	11.12.2017	

**Education Qualification**

Degree	Name of the Institution	University	Year of Passing
Ph.D.	-	-	-
M.E.	VEL MULTI TECH DR.RR & DR.SR ENGINEERING COLLEGE, CHENNAI	ANNA UNIVERSITY CHENNAI	2011
B.TECH.	SASTRA UNIVERSITY, THANJAVUR	SASTRA UNIVERSITY	2009

**Experience Details**

Institution/ Company	Designation / Position	Duration	Experience in Years	Cumulative Experience
<b>M.A.M College of Engineering and Technology</b>	Assistant Professor	11.12.2017 to Till date	1 year 1 month	1 year 1 month 14 days
<b>Mahendra Engineering College(Autonomous)</b>	Assistant Professor	10.06.2015 to 29.04.2017	1 Year 10 Months	3 Years 3 days
<b>Vetri Vinayaha College of Engineering And Technology</b>	Assistant Professor	16.06.2014 to 27.05.2015	11 months	3 Years 11 Months 14 days
<b>M.Kumarasamy Engineering College(Autonomous)</b>	Assistant Professor	23.12.2011 to 07.05.2014	2 Years 4 Months	6 Years 3 Months 29 days
<b>Kongunadu College of Engineering and Technology</b>	Assistant Professor	01.06.2011 to 30.09.2011	3 Months	6 years 8 Months

**Publications Details**

Category	Journal	Conference	Total
National	-	5	6
International	2	4	6
Consultancy	-	<b>Total Publications</b>	12

Product Development	-	Ph.D. Guidance	-
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Workshop/Conference/FDP/Seminar Participation :

1. Workshop & Seminars :12
2. Conferences(National & International) : 9
3. FDP :10

Professional Body Membership :ISTE Life Time Member  
M.No:LM 96251

Awards and Achievements :

- Cleared **GATE Exam** with **82 Percentile** in 2009.
- Did ME project in **DRDO Hyderabad**.
- Received an award and Cash Prize for Producing University Rank for the subject **“Microcontroller and RISC Architecture”** and **“Wireless networks”** at M.Kumarasamy College of Engineering-Karur.
- Got **ELITE** in NPTEL Online Certification Course(12 weeks)**“Microprocessors and Microcontrollers”** with a consolidated Score of **72%**.
- Got **ELITE** in NPTEL Online Certification Course(12 weeks) **“Digital Circuits”**with a consolidated Score of **80%**.
- Got **ELITE** in NPTEL Online Certification Course(12 weeks) **“Control Systems”**with a consolidated Score of **70%**.
- Department Topper in **Mathematics** during 3<sup>rd</sup> semester in B.Tech.

**International Conference:**

1. Presented a paper in **“International conference”** held at **Vetri Vinayaha College of Engineering And Technology-Namakkal** on 28<sup>th</sup> February 2015.

**Title:** PDP Reduction Using Single Event Transient For Sequential Circuit.

2. Presented a paper in **“International conference”** held at **Vetri Vinayaha College of Engineering And Technology-Namakkal** on 28<sup>th</sup> February 2015.

**Title:** Enhanced THz Antenna Design With Improved Directivity And Radiation Efficiency.

3. Presented a paper in **“International conference”** held at **K.S.R College of Engineering –Tiruchengode** on 22<sup>nd</sup> February 2016.

**Title:** Memristor Modelling and Optimization for Ultra Low Power Applications.

4. Presented a paper in **“International conference”** Organized by **Society of Engineering and technology & Newton tech Solution Tirunelveli INCETET 2015** on 5<sup>th</sup> December 2015.

**Title:** Analysis of Sub Threshold Devices to Low Voltage fault attacks.

**National Conference:**

1. Presented a Paper in **“National Conference”** held in **Francis Xavier Engineering College-Tirunelveli** on 8<sup>th</sup> April 2010.

**Title:** Multipath Routing For Video Communication Over Wireless Network.

2. Presented a Project Paper in “**98<sup>th</sup> Indian Science Congress(International Event)**” held in **SRM University, Chennai** on 3-7 January 2011.

**Title:** Synthesis And Gate Level Simulation Of Avionics Custom Peripherals Using Synopsys.

3. Presented a Project Paper in “**National Conference**” held in **S.A Engineering College- Chennai** on 5<sup>th</sup> February 2011.

**Title:** Synthesis and Gate level simulation of Avionics Custom Peripherals using Synopsys.

4. Presented a Paper in “**National Conference**” held in **PSNA college of Engineering & Technology** on 21<sup>st</sup> March 2014.

**Title:** Enhancement of Delay & Power for Sequential Elements using Transient Suppressor.

5. Presented a Paper in “**National Conference**” held in **K.S.Rangasamy College of Technology** on 25<sup>th</sup> March 2014.

**Title:** Enhancement of Delay & Power for Sequential Circuit using SET

#### International Journals:

1. Published a Paper in International Journal “**International Journal of Advances in Engineering Research-IJAER**” .

**Title:** Analysis of Sub Threshold Devices to Low Voltage fault attacks.

2. Published a Paper in International Journal “**International Journal of Advances in Engineering Research-IJAER**” .

**Title:** Memristor Modelling and Optimization for Ultra Low Power Applications.

#### National Journals:

NIL

#### Others:

- Coordinators for various **International and National Conferences**.
- Jury for various **paper presentations** held in various Engineering Colleges.
- **Counselled many UG Students** for their academic excellence.
- Guided many **UG and PG projects**.
- **Class Coordinator/Advisor** for UG and PG Students.
- **YI-YUVA Coordinator for MAMCET** from June 2018 and Conducted more than 10 Entrepreneurship Development Program and many Business plan events for UG Students.
- Submitted many **project proposals for funding agencies** and received a fund of **Rs.1.5 lakhs from AICTE** for research proposal.
- Gave Motivational talks to many School Students and UG Engineering Students of various Engineering Colleges .