




M.A.M. COLLEGE OF ENGINEERING AND TECHNOLOGY
Siruganur, Tiruchirappalli 621 105.
(Approved by AICTE & Affiliated to Anna University)
FACULTY PROFILE

Name	K. Karpoora Sundari	Email	kksundari@gmail.com	
Qualification	M.E	AU Faculty Code	8120047	
Designation	Associate Professor	Mobile Number	9944263357	
Area of Specialization	Applied Electronics	Experience at MAMCET	6 Years 8 Months	
Experience	11 Years 8 Months	Joining Date	01.06.2012	

Education Qualification

Degree	Name of the Institution	University	Year of Passing
Ph.D.	-	-	-
M.E.	R. V. S College of Engineering & Technology	Anna University, Chennai	2012
B.E.	Syed Ammal Engineering College	Madurai Kamaraj University	2003

Experience Details

Institution/ Company	Designation / Position	Duration	Experience in Years	Cumulative Experience
M.A.M College Of Engineering & Technology	Associate Professor	01.06.2012 to till now	6.8	13.3
Sudharsan Engineering College, Pudukkottai.	Lecturer	16.06.2008 to 10.10.2011	3.3	6.5
Dhanalakshmi Srinivasan Engineering College, Perambalur	Lecturer	08.08.2006 to 27.04.2018	1.7	3.2
Gems Pro Consultancy Services	Software Programmer	15/06/2004 to 26/07/2006	1.11	2.5
Ibilt Technologies	Tech Support Engineer	12/08/2003 to 15/01/2004	0.6	0.6

Publications Details

Category	Journal	Conference	Total
National			
International			
Consultancy		Total Publications	
Product Development		Ph.D. Guidance	-

Workshop/Conference/FDP/Seminar Participation :

- Attended the Anna University sponsored FDP on “EC6602 – Antenna & Wave Propagation” from 30/11/2015 to 06/12/2015 in M. A. M College of Engineering & Technology, Siruganur, Trichy.
- Attended the 2 Days FDP on “Build Your Own Embedded System” conducted by ICT Academy of Tamil Nadu from 19/03/2015 to 20/03/2015 held at Saranathan Engineering College, Tiruchirappalli.
- Attended National level “AICTE sponsored staff development programme on evolving trends in optical communication networks” from 08/05/2013 to 21/05/2013 in Sudharsan engineering college, Sathiyamangalam, Pudukkottai District.
- Attended two days workshop on “NS2 Simulator” in Trichy engineering college, Trichy.
- Attended one day national level workshop on “Analog & Mixed signal IC design” on 11th February, 2012 at Sudharsan Engineering College, Sathiyamangalam, Pudukkottai District.
- Attended workshop “Dale Carnegie Seminar” in April, 2011 in Sudharsan Engineering College, Sathiyamangalam, Pudukkottai District.
- Attended two days national workshop on “Recent Trends In Communication Technology” from 24th – 25th September, 2010 in Sudharsan Engineering College, Sathiyamangalam, Pudukkottai District.
- Attended the workshop on “Advanced Communication Skill” under the Faculty Development Programme of the college from 14th May, 2009 to 16th May, 2009 in Sudharsan Engineering College, Sathiyamangalam, Pudukkottai District.
- Attended one day STEP – 1 training organized by the college on 24th June, 2008 at Fortune Pandian, Madurai.
- Attended National level seminar on “Embedded Systems” on 1st March, 2008 in Dhanalakshmi Srinivasan Engineering College, Perambalur.
- Attended two days national level workshop on “Signal & Image Processing using MATLAB” Dhanalakshmi Srinivasan Engineering College, Perambalur.
- Attended AICTE Sponsored Staff Development Programme on “Learning Systems Intelligent Control: An appraisal of Fuzzy, Neural and Genetic Algorithms” From 12th March, 2007 to 24th March, 2007 in St. Peters Engineering College, Avadi, Chennai – 600 054.
- Attended the workshop on “Digital ASIC design flow and FPGA implementation” during 11th to 15th June, 2007, organized by IC design Group of school of Electrical Sciences in VIT University, Vellore – 632014.
- Attended National level seminar on “Recent trends in Analog and Digital VLSI Design” on 1st March, 2007 in Dhanalakshmi Srinivasan Engineering College, Perambalur.
- Attended one day workshop on “Computer Networking” on 14th November, 2006 in Anna University, Department of Electronics & Communication Engineering , College of Engineering, Guindy, Chennai – 600 025.
- Attended one day workshop on “Network Cryptanalysis” on 9th October, 2006 in Dhanalakshmi Srinivasan Engineering College, Perambalur.

Professional Body Membership : - NIL

Awards and Achievement :

- Worked as mentor for the projects in Gamesa Wind Turbines Pvt. Ltd., Chennai.
- I have successfully completed the course on “Fundamentals of Electronic Materials and Devices” through NPTEL online certification, sponsored by Indian Institute of Technology Madras.
- Presently acting as an ISO coordinator & Assistant HoD from 1st February 2014 to till date in ECE.
- Organized various National level Seminars, FDPs, Workshop and technical symposium.
- One of the organizing committee member for conducting international level conference on “ISTE – CON 2008 Trichy” during 13th to 14th August, 2008.
- Worked in various AICTE sponsored projects like seminars, FDPs, Workshops, MODROB Grants, Anna University proposals & CSIR proposals.
- Acted as an NBA Coordinator of ECE Department in Sudharsan Engineering College, Sathiyamangalam.
- Acted as an ISO Coordinator of ECE Department in Sudharsan Engineering College, Sathiyamangalam.
- Acted as a warden in Sudharsan Engineering College, Sathiyamangalam.
- Acted as a deputy warden in Dhanalakshmi Srinivasan Engineering College, Perambalur.
- Acted as one of the organizing member in ECE department during NBA accreditation in Dhanalakshmi Srinivasan Engineering College, Perambalur.
- Achieved 100 percent result for the subjects embedded systems, High Speed Networks & Television Engineering, Design of Embedded systems, Real time embedded systems, ASIC Design.
- Achieved 99 percent result for the subject VLSI Design.

International Conferences

- Presented a Paper in the IEEE Sponseres International Level Conference on “ICSSC- 19” in the title of “ Artificial Intelligence and its influences in System on Chip Design” in Anna University, CEG Campus, Chennai.
- Presented a paper in the IEEE Sponsored International level conference on “ ICEEICA – 16 ” in the title of “Low Power PRPG and Decompressor using PRESTO Generator”, in Selvam College of Technology, Namakkal – 637003.

National Conference:

- Presented a Paper in the national level conference on “Advances and Innovative Ideas on Information and Communication, VLSI and Embededd Technologies in the title of “ Design and Implementation of BZ-FAD Multiplier based on shift and Add Architecture” on 30th March, 2012 in R. V. S College of Engineering & Technology, RVS Nagar, Karur Road,Dindigul – 624 005.

- Presented a Paper in the national level conference on “Innovations in Electrical, Electronics and Communication Engineering in the title of “ Comparison and Analysis of of BZ-FAD based on shift and Add Architecture with various multipliers” on 22nd March, 2012 in Roever Engineering College, Elambalur, Perambalur – 621 212. Tamil Nadu.
- Presented a Paper in the 2nd national level conference on “ signal Processing, Electronics, Communication trends in Recent Advancements – SPECTRA '12 ” in the title of “on “A Low power Low Area Multiplier using BZFAD algorithm for DSP Applications ”on 16th March, 2012 in Bharath Niketan Engineering College, Sri Gowri Nagar, Thimmarasa Naiackanoor, Aundipatty – 625 536, Theni District, Tamil Nadu.
- Presented a Paper in the CSIR sponsored national level conference on “ Recent Advances in VLSI and Embedded Systems (RAVE) ” in the title of “An Efficient 180- NM CMOS SRAM using self controllable voltage level Technique ”on 23rd Feburary, 2011 in sudharsan Engineering College, Sathiyamangalam, Pudukkottai District., Tamil Nadu.

Others: